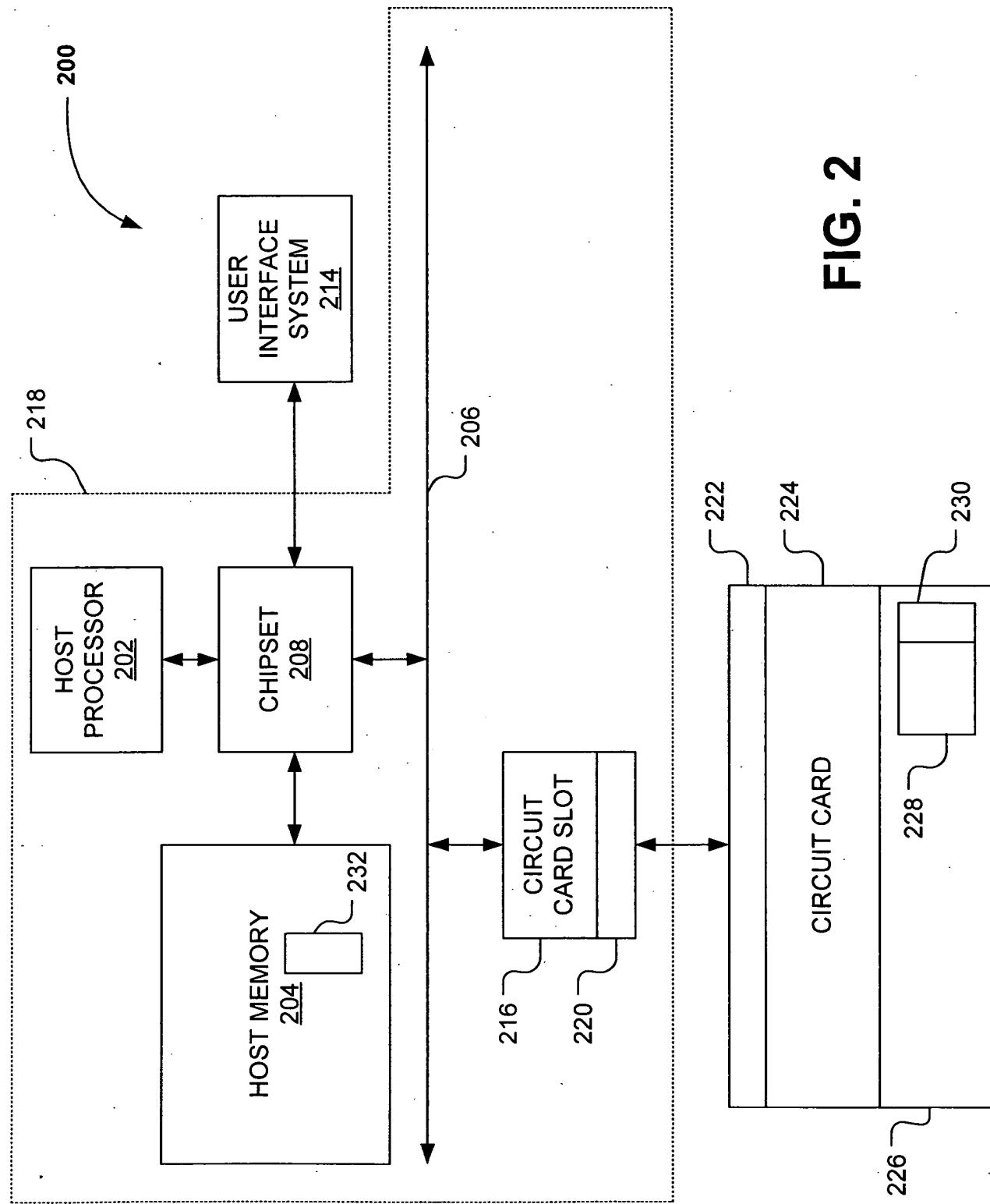


FIG. 1



TITLE: LOAD BALANCING DEVICE COMMUNICATIONS
INVENTOR: CHET R. DOUGLAS
ATTORNEY DOCKET NO.: P17149
CONTACT: LIBBY HOPE, 46,774/INTEL CORPORATION/949-498-0601

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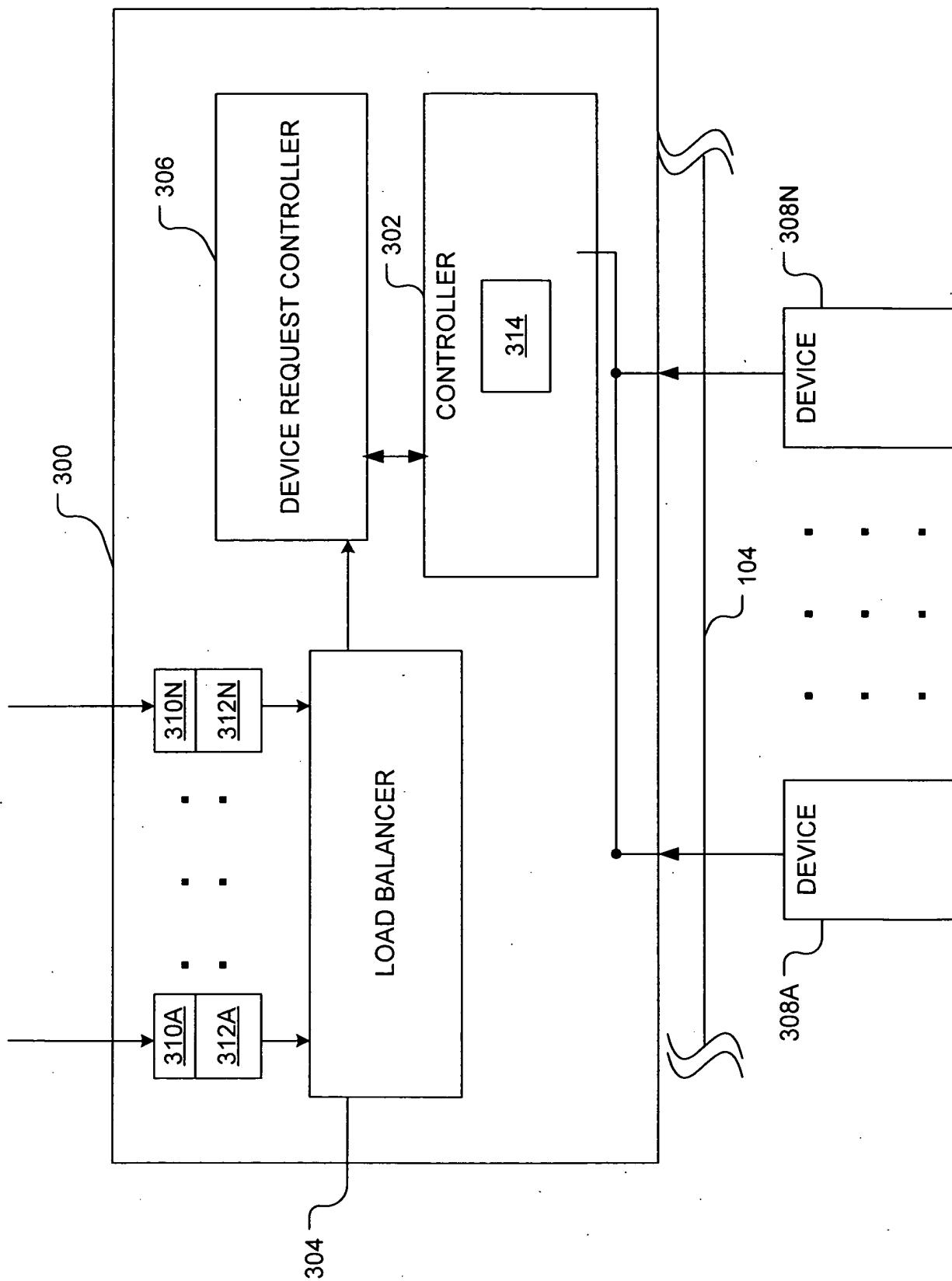


FIG. 3

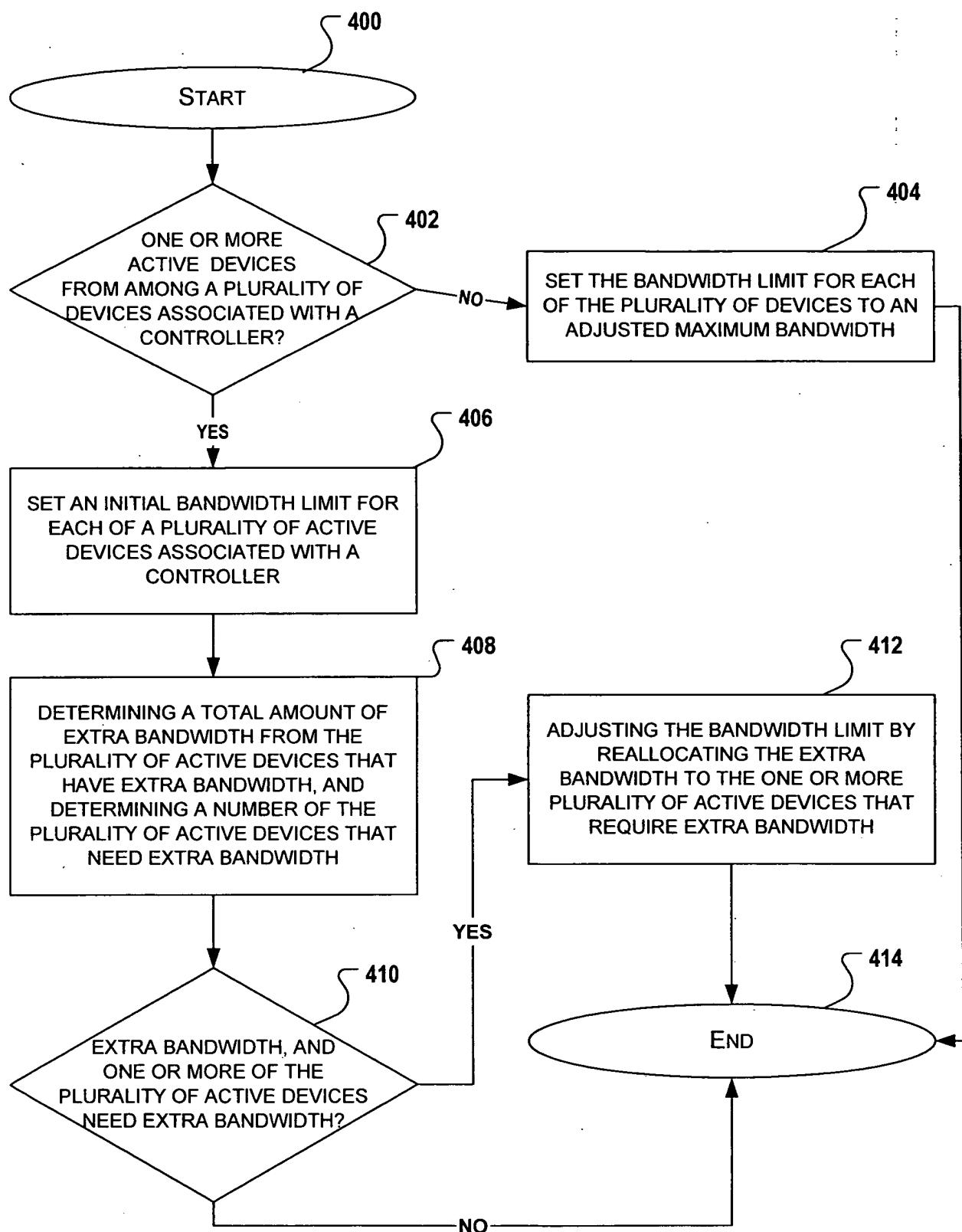


FIG. 4

MAX BANDWIDTH = 82	RES BANDWIDTH = 2	MIN BANDWIDTH = 10	DEVICE COUNT = 4
<u>560</u>	<u>562</u>	<u>564</u>	<u>566</u>
<u>500</u>	<u>502</u>	<u>504</u>	<u>508</u>
PRIORITY[500] = 0 (510)	PRIORITY[502] = 0 (520)	PRIORITY[504] = 0 (530)	PRIORITY[508] = 0 (550)
TOTAL REQUESTED BANDWIDTH[500] = 10 (512)	TOTAL REQUESTED BANDWIDTH[502] = 5 (522)	TOTAL REQUESTED BANDWIDTH[504] = 60 (532)	TOTAL REQUESTED BANDWIDTH[508] = 0 (552)
QUEUE BANDWIDTH[500] = 10 (514)	QUEUE BANDWIDTH[502] = 0 (524)	QUEUE BANDWIDTH[504] = 40 (534)	QUEUE BANDWIDTH[508] = 0 (554)
ACTIVE BANDWIDTH[500] = 0 (516)	ACTIVE BANDWIDTH[502] = 5 (526)	ACTIVE BANDWIDTH[504] = 20 (536)	ACTIVE BANDWIDTH[508] = 0 (556)

FIG. 5

TITLE: LOAD BALANCING DEVICE COMMUNICATIONS

INVENTOR: CHET R. DOUGLAS

ATTORNEY DOCKET NO.: P17149

CONTACT: LIBBY HOPE, 46,774/INTEL CORPORATION/949-498-0601

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MAX BANDWIDTH = 82 <u>560</u>	RES BANDWIDTH = 2 <u>562</u>	MIN BANDWIDTH = 10 <u>564</u>	DEVICE COUNT = 4 <u>566</u>
PRIORITY[500] = 0 (510)	PRIORITY[502] = 0 (520)	PRIORITY[504] = 0 (530)	PRIORITY[506] = 0 (540)
TOTAL REQUESTED BANDWIDTH[500] = 10 (512)	TOTAL REQUESTED BANDWIDTH[502] = 5 (522)	TOTAL REQUESTED BANDWIDTH[504] = 60 (532)	TOTAL REQUESTED BANDWIDTH[506] = 50 (542)
QUEUE BANDWIDTH[500] = 10 (514)	QUEUE BANDWIDTH[502] = 0 (524)	QUEUE BANDWIDTH[504] = 40 (534)	QUEUE BANDWIDTH[506] = 30 (544)
ACTIVE BANDWIDTH[500] = 0 516	ACTIVE BANDWIDTH[502] = 5 526	ACTIVE BANDWIDTH[504] = 20 536	ACTIVE BANDWIDTH[506] = 20 546
ACTIVE BANDWIDTH LIMIT[500] = 20 600	ACTIVE BANDWIDTH LIMIT[502] = 20 602	ACTIVE BANDWIDTH LIMIT[504] = 20 604	ACTIVE BANDWIDTH LIMIT[506] = 20 <u>606</u>

FIG. 6

TITLE: LOAD BALANCING DEVICE COMMUNICATIONS
INVENTOR: CHET R. DOUGLAS
ATTORNEY DOCKET NO.: P17149
CONTACT: LIBBY HOPE, 46,774/INTEL CORPORATION/949-498-0601

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MAX BANDWIDTH = 82 <u>560</u>	RES BANDWIDTH = 2 <u>562</u>	MIN BANDWIDTH = 10 <u>564</u>	DEVICE COUNT = 2 <u>566</u>
AVG BANDWIDTH = 20 <u>610</u>	TOTAL EXTRA BANDWIDTH = 25 <u>712</u>	PRIORITY COUNT = 0 <u>710</u>	PRIORITY COUNT = 0 <u>508</u>
<u>500</u>	<u>504</u>	PRIORITY[506] = 0 (540)	PRIORITY[508] = 0 (550)
<u>502</u>	<u>506</u>	TOTAL REQUESTED BANDWIDTH[506] = 50 (542)	TOTAL REQUESTED BANDWIDTH[508] = 0 (552)
PRIORITY[500] = 0 (510)	PRIORITY[504] = 0 (530)	QUEUE BANDWIDTH[506] = 30 (544)	QUEUE BANDWIDTH[508] = 0 (554)
TOTAL REQUESTED BANDWIDTH[502] = 5 (522)	TOTAL REQUESTED BANDWIDTH[504] = 60 (532)	ACTIVE BANDWIDTH[506] = 20 <u>546</u>	ACTIVE BANDWIDTH[508] = 0 <u>556</u>
GIVE BW = 15 (702)	GIVE BW = 15 (702)	QUEUE BANDWIDTH[504] = 40 (534)	ACTIVE BANDWIDTH[508] = 0 (554)
QUEUE BANDWIDTH[500] = 10 (512)	QUEUE BANDWIDTH[502] = 0 (524)	ACTIVE BANDWIDTH[506] = 20 <u>536</u>	ACTIVE BANDWIDTH[508] = 0 (556)
GIVE BW = 10 (700)	GIVE BW = 10 (700)	ACTIVE BANDWIDTH[504] = 5 <u>526</u>	ACTIVE BANDWIDTH[508] = 0 (558)
QUEUE BANDWIDTH[500] = 10 (514)	ACTIVE BANDWIDTH[502] = 5 <u>526</u>	ACTIVE BANDWIDTH[506] = 20 <u>602</u>	ACTIVE BANDWIDTH[508] = 20 <u>608</u>
ACTIVE BANDWIDTH[500] = 0 <u>516</u>	ACTIVE BANDWIDTH[504] = 5 <u>526</u>	ACTIVE BANDWIDTH[506] = 20 <u>604</u>	ACTIVE BANDWIDTH[508] = 20 <u>608</u>
ACTIVE BANDWIDTH LIMIT[500] = 20 <u>600</u>	ACTIVE BANDWIDTH LIMIT[502] = 20 <u>602</u>	ACTIVE BANDWIDTH LIMIT[506] = 20 <u>606</u>	ACTIVE BANDWIDTH LIMIT[508] = 20 <u>608</u>

FIG. 7

MAX BANDWIDTH = 82 <u>560</u>	MIN LOAD = 10 <u>564</u>	DEVICE COUNT = 2 <u>560</u>
AVG BANDWIDTH = 20 <u>610</u>	TOTAL EXTRA BANDWIDTH = 25 <u>712</u>	ADD COUNT = 12 <u>800</u>
<u>500</u>	<u>502</u>	PRIORITY[502] = 0 (520)
PRIORITY[500] = 0 (510)	<u>504</u>	PRIORITY[504] = 0 (530)
TOTAL REQUESTED BANDWIDTH[502] = 10 (<u>512</u>) GIVE BW = 10 (<u>700</u>)	<u>506</u>	PRIORITY[506] = 0 (540)
QUEUE BANDWIDTH[502] = 10 (<u>514</u>)	<u>508</u>	PRIORITY[508] = 0 (550)
TOTAL REQUESTED BANDWIDTH[504] = 5 (<u>522</u>) GIVE BW = 15 (<u>702</u>)	<u>510</u>	TOTAL REQUESTED BANDWIDTH[508] = 0 (552)
QUEUE BANDWIDTH[504] = 0 (<u>524</u>)	<u>512</u>	QUEUE BANDWIDTH[508] = 0 (554)
ACTIVE BANDWIDTH[502] = 5 <u>526</u>	<u>514</u>	ACTIVE BANDWIDTH[508] = 0 <u>556</u>
ACTIVE BANDWIDTH[500] = 0 <u>516</u>	<u>516</u>	ACTIVE BANDWIDTH[508] = 0 <u>556</u>
ACTIVE BANDWIDTH LIMIT[502] = 5 <u>602</u>	<u>518</u>	ACTIVE BANDWIDTH LIMIT[508] = 32 <u>606</u>
ACTIVE BANDWIDTH LIMIT[500] = 10 <u>600</u>	<u>520</u>	ACTIVE BANDWIDTH LIMIT[508] = 0 <u>608</u>

FIG. 8

TITLE: LOAD BALANCING DEVICE COMMUNICATIONS

INVENTOR: CHET R. DOUGLAS

ATTORNEY DOCKET NO.: P17149

CONTACT: LIBBY HOPE, 46,774/INTEL CORPORATION/949-498-0601

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MAX BANDWIDTH = 82	RES BANDWIDTH = 2	MIN BANDWIDTH = 10	DEVICE COUNT = 4
<u>900</u>	<u>902</u>	<u>904</u>	<u>908</u>
PRIORITY[900] = 0 (<u>910</u>)	PRIORITY[902] = 0 (<u>920</u>)	PRIORITY[904] = 1 (<u>940</u>)	PRIORITY[908] = 0 (<u>950</u>)
TOTAL REQUESTED BANDWIDTH[900] = 10 (<u>912</u>)	TOTAL REQUESTED BANDWIDTH[902] = 5 (<u>922</u>)	TOTAL REQUESTED BANDWIDTH[904] = 60 (<u>932</u>)	TOTAL REQUESTED BANDWIDTH[908] = 0 (<u>952</u>)
QUEUE BANDWIDTH[900] = 10 (<u>914</u>)	QUEUE BANDWIDTH[902] = 0 (<u>924</u>)	QUEUE BANDWIDTH[904] = 40 (<u>934</u>)	QUEUE BANDWIDTH[908] = 0 (<u>954</u>)
ACTIVE BANDWIDTH[900] = 0 (<u>916</u>)	ACTIVE BANDWIDTH[902] = 5 (<u>926</u>)	ACTIVE BANDWIDTH[904] = 20 (<u>936</u>)	ACTIVE BANDWIDTH[908] = 0 (<u>956</u>)

FIG. 9

TITLE: LOAD BALANCING DEVICE COMMUNICATIONS

INVENTOR: CHET R. DOUGLAS

ATTORNEY DOCKET NO.: P17149

CONTACT: LIBBY HOPE, 46,774/INTEL CORPORATION/949-498-0601

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MAX BANDWIDTH = 82 <u>960</u>	RES BANDWIDTH = 10 <u>964</u>	DEVICE COUNT = 4 <u>966</u>
AVG BANDWIDTH = 20 <u>1010</u>	PRIORITy[900] = 0 (910)	PRIORITy[904] = 0 (920)
TOTAL REQUESTED BANDWIDTH[900] <u>= 10 (912)</u>	TOTAL REQUESTED BANDWIDTH[902] <u>= 5 (922)</u>	PRIORITy[902] = 0 (920)
QUEUE BANDWIDTH[900] <u>= 10 (914)</u>	QUEUE BANDWIDTH[902] <u>= 0 (924)</u>	902
ACTIVE BANDWIDTH[900] <u>= 0 916</u>	ACTIVE BANDWIDTH[904] <u>= 20 926</u>	PRIORITy[904] = 0 (930)
ACTIVE BANDWIDTH LIMIT[900] = 20 <u>1000</u>	ACTIVE BANDWIDTH LIMIT[902] = 20 <u>1002</u>	TOTAL REQUESTED BANDWIDTH[906] <u>= 80 (942)</u>
ACTIVE BANDWIDTH LIMIT[902] = 20 <u>1002</u>	QUEUE BANDWIDTH[904] <u>= 40 (934)</u>	PRIORITy[906] = 1 (940)
ACTIVE BANDWIDTH LIMIT[904] = 20 <u>1004</u>	ACTIVE BANDWIDTH[906] <u>= 20 946</u>	TOTAL REQUESTED BANDWIDTH[908] <u>= 0 (952)</u>
ACTIVE BANDWIDTH LIMIT[906] = 20 <u>1006</u>	ACTIVE BANDWIDTH[908] <u>= 0 956</u>	906
ACTIVE BANDWIDTH LIMIT[908] = 0 <u>1008</u>	QUEUE BANDWIDTH[908] <u>= 0 (954)</u>	PRIORITy[908] = 0 (950)
		908

FIG. 10

TITLE: LOAD BALANCING DEVICE COMMUNICATIONS

INVENTOR: CHET R. DOUGLAS

ATTORNEY DOCKET NO.: P17149

CONTACT: LIBBY HOPE, 46,774/INTEL CORPORATION/949-498-0601

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MAX BANDWIDTH = 82 <u>960</u>	RES BANDWIDTH = 2 <u>962</u>	MIN BANDWIDTH = 10 <u>964</u>
AVG BANDWIDTH = 20 <u>1010</u>	TOTAL EXTRA BANDWIDTH = 25 <u>1112</u>	PRIORITY COUNT = 1 <u>1110</u>
<u>900</u>	<u>902</u>	<u>904</u>
PRIORITY[900] = 0 (910)	PRIORITY[902] = 0 (920)	PRIORITY[904] = 0 (930)
TOTAL REQUESTED BANDWIDTH[900] = 10 (<u>912</u>) GIVE BW = 10 (<u>1100</u>)	TOTAL REQUESTED BANDWIDTH[902] = 5 (<u>922</u>) GIVE BW = 15 (<u>1102</u>)	TOTAL REQUESTED BANDWIDTH[904] = 60 (932)
QUEUE BANDWIDTH[900] = 10 (<u>914</u>)	QUEUE BANDWIDTH[904] = 40 (934)	QUEUE BANDWIDTH[906] = 30 (<u>944</u>)
ACTIVE BANDWIDTH[900] = 0 <u>916</u>	ACTIVE BANDWIDTH[904] = 20 <u>936</u>	ACTIVE BANDWIDTH[906] = 20 <u>946</u>
ACTIVE BANDWIDTH LIMIT[900] = 20 <u>1000</u>	ACTIVE BANDWIDTH LIMIT[904] = 20 <u>1004</u>	ACTIVE BANDWIDTH LIMIT[906] = 20 <u>1006</u>
DEVICE COUNT = 2 <u>966</u>	PRIORITY COUNT = 1 <u>1110</u>	PRIORITY(908) = 0 (950) <u>908</u>
		TOTAL REQUESTED BANDWIDTH[908] = 0 (952) <u>908</u>
		QUEUE BANDWIDTH[908] = 0 (954) <u>908</u>
		ACTIVE BANDWIDTH[908] = 0 <u>956</u>
		ACTIVE BANDWIDTH LIMIT[908] = 0 <u>1008</u>

FIG. 11

TITLE: LOAD BALANCING DEVICE COMMUNICATIONS

INVENTOR: CHET R. DOUGLAS

ATTORNEY DOCKET NO.: P17149

CONTACT: LIBBY HOPE, 46,774/INTEL CORPORATION/949-498-0601

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MAX BANDWIDTH = 82 <u>960</u>	RES BANDWIDTH = 2 <u>962</u>	MIN BANDWIDTH = 10 <u>964</u>	DEVICE COUNT = 2 <u>966</u>
AVG BANDWIDTH = 20 <u>1010</u>	TOTAL EXTRA BANDWIDTH = 25 <u>1112</u>	ADD COUNT = 25 <u>1200</u>	PRIORITY COUNT = 1 <u>1110</u>
<u>900</u>	<u>902</u>	<u>904</u>	<u>908</u>
PRIORITY[900] = 0 (910)	PRIORITY[902] = 0 (920)	PRIORITY[904] = 0 (930)	PRIORITY[908] = 0 (950)
TOTAL REQUESTED BANDWIDTH[900] = 10 (912) GIVE BW = 10 (1100)	TOTAL REQUESTED BANDWIDTH[902] = 5 (922) GIVE BW = 15 (1102)	TOTAL REQUESTED BANDWIDTH[904] = 60 (932)	TOTAL REQUESTED BANDWIDTH[906] = 50 (942)
QUEUE BANDWIDTH[900] = 0 ^{0.14} ACTIVE BANDWIDTH[900] = 0 ^{0.14} 916	QUEUE BANDWIDTH[902] = 0 ^{0.24} ACTIVE BANDWIDTH[902] = 5 ^{0.24} 926	QUEUE BANDWIDTH[904] = 40 ^{0.24} ACTIVE BANDWIDTH[904] = 20 ^{0.24} 936	QUEUE BANDWIDTH[906] = 30 ^{0.44} ACTIVE BANDWIDTH[906] = 20 ^{0.44} 946
ACTIVE BANDWIDTH LIMIT[900] = 10 <u>1000</u>	ACTIVE BANDWIDTH LIMIT[902] = 5 <u>1002</u>	ACTIVE BANDWIDTH LIMIT[904] = 45 <u>1004</u>	ACTIVE BANDWIDTH LIMIT[908] = 0 <u>1008</u>

FIG. 12